

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~striketrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please CANCEL claims 8-15, without prejudice.

1. (ORIGINAL) A semiconductor device having an actual pattern and plural types of dummy patterns on a wiring layer thereof,

wherein said plural types of dummy patterns on the wiring layer have at least either of a different size or a different shape from each other for the type of said dummy pattern.

2. (ORIGINAL) The semiconductor device according to claim 1,
wherein said dummy patterns are different in size for each type.

3. (ORIGINAL) The semiconductor device according to claim 1,
wherein said dummy patterns have rectangular shapes with same longer sides and different shorter sides in length, the shorter sides being different from each other for each type.

4. (ORIGINAL) A semiconductor device having an actual pattern, a first dummy pattern, and a second dummy pattern on a wiring layer thereof,
wherein said second dummy pattern has at least either of a different shape or a different size from corresponding that of the first dummy pattern.

5. (ORIGINAL) The semiconductor device according to claim 4,
wherein said second dummy pattern is smaller than said first dummy pattern.

6. (ORIGINAL) The semiconductor device according to claim 5,
wherein said second dummy pattern has a rectangular shape.

7. (ORIGINAL) The semiconductor device according to claim 5,
wherein said first dummy pattern has a square shape and said second dummy pattern

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has a rectangular shape with same longer sides in length as one side of said first dummy pattern and shorter sides which are shorter than said one side.

8. (CANCELLED)

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